



The International Conference on Dependable Systems and Networks

Yokohama, Japan

June 28 - July 1, 2005

Sponsored by: IEEE Computer Society Technical Committee on Fault-Tolerant Computing
IFIP WG 10.4 on Dependable Computing and Fault Tolerance

In cooperation with: IEICE Technical Group on Dependable Computing
Research Center for Advanced Science and Technology, The University of Tokyo

CALL FOR CONTRIBUTIONS



The 2005 International Conference on Dependable Systems and Networks (DSN-2005), the leading international conference on dependable computing, will be held in Yokohama, Japan, from June 28th to July 1st, 2005. Dependability is a vital attribute for computing systems and communication networks that pervade every aspect of modern societies and our daily lives. As we have increasingly relied on correct and safe functioning of these systems and networks, there have been increasing concerns about malicious exploitation of imperfect systems and networks and intentional cyber-attacks as well as the traditional concerns for inadvertent or accidental faults, errors, and failures. This conference attempts to provide answers to the question "how can we make computer systems and networks that we increasingly rely on more dependable?"

The conference will have multiple main tracks of regular papers as well as practical experience reports, software, system and tool demonstrations, and panels on topics of current interest, including the Dependable Computing and Communications Symposium (DCCS) and the Performance and Dependability Symposium (PDS). There will also be several Workshops, Tutorials, a Student Forum, Fast Abstracts to emphasize late-breaking research. These activities provide a comprehensive program for researchers, practitioners, managers and students to learn and exchange information on the latest research results and state of the art in dependable systems and networks.

Submission Dates

Papers: **November 19, 2004**

Tutorials: **January 14, 2005**

Panel proposals: **November 19, 2004**

Student Forum: **April 1, 2005**

Workshop Proposals: **October 8, 2004**

Fast Abstracts: **April 1, 2005**

Visit us on the web at <http://www.dsn.org/> !

Categories for Contributions

DSN 2005 features a number of coordinated tracks, including: the Dependable Computing and Communication Symposium (DCCS); the Performance and Dependability Symposium (PDS); several Workshops; Tutorials; a Student Forum; and Fast Abstracts. We invite you to contribute to this premier forum by submitting in one or more of the categories outlined below:

Regular Papers	report on recent research results	Topics (include, but are not limited to): Architectures for Dependable Computer Systems Dependability Benchmarking Dependability of High-Speed Networks and Protocols Dependability Modeling and Prediction Dependability in VLSI E-Commerce Dependability Fault-Tolerance in Transaction Processing Fault-Tolerance in Distributed and Real-Time Systems Fault-Tolerance in Multimedia Systems Fault-Tolerance in Mobile Systems Internet Dependability and Quality of Service Survivable and Intrusion-Tolerant Systems Safety-Critical Systems Software Testing, Validation, and Verification Software Reliability Analytical and Simulation Techniques for Performance and Dependability Assessment Measurement Techniques for Performance and Dependability Assessment Tools for Performance and Dependability Assessment
Practical Experience Reports	provide in-depth description of an experience or a case study	
Demonstrations: Software, System and Tools	describe the software, system, or tool, its context and objective, and the planned demonstration	
Panels	discuss and debate topics of high interest to the community	
Workshops	provide opportunities for focused discussion on selected topics in a small group setting	
Tutorials	provide a short course on dependability practice, techniques, and theories	
Student Forum	provides an opportunity for Ph.D. students to present their research	
Fast Abstracts	present work-in-progress	

Organizing Committee

General Chair:	Takashi Nanya (Univ. of Tokyo, Japan)	<nanya@hal.rcast.u-tokyo.ac.jp>
Honorary General Chair:	Yoshihiro Tohma (Tokyo Denki Univ., Japan)	<tohma@sie.dendai.ac.jp>
Conference Coordinator:	Tohru Kikuno (Osaka Univ., Japan)	<kikuno@ist.osaka-u.ac.jp>
Fund-raising Chair:	Katsuji Akita (Railway Technical Research Institute)	<akita@rtri.or.jp>
Finance Chair:	Nobuyasu Kanekawa (Hitachi, Japan)	<kanekawa@gm.hrl.hitachi.co.jp>
Local arrangement Chair:	Kiyoshi Furuya (Chuo Univ., Japan)	<furuya@ise.chuo-u.ac.jp>
Publicity Chair:	Haruo Yokota (Tokyo Inst. of Tech., Japan)	<yokota@cs.titech.ac.jp>
Registration Chair:	Tomohiro Yoneda (NII, Japan)	<yoneda@nii.ac.jp>
Publication Chair:	Tatsuhiko Tsuchiya (Osaka Univ., Japan)	<t-tutiya@ist.osaka-u.ac.jp>
Tutorials Chair:	Zbigniew Kalbarczyk (Univ. of Illinois, USA)	<kalbar@crhc.uiuc.edu>
Workshops Chair:	Nuno Ferreira Neves (Univ. of Lisbon, Portugal)	<nuno@di.fc.ul.pt>
Student Forum Chair:	Philip Koopman (CMU, USA)	<koopman@cmu.edu>
Fast Abstracts Chair:	Matti Hiltunen (AT&T Labs - Research, USA)	<hiltunen@research.att.com>
Exhibition Chair:	Kohsuke Tatsukawa (NEC, Japan)	<tatsu@ab.jp.nec.com>
Industry Liaison Chair:	Yasunori Kimura (Fujitsu, Japan)	<ykimura@jp.fujitsu.com>
Academia Liaison Chair:	Kazuhiko Iwasaki (Tokyo Metropolitan Univ.)	<iwasaki@eei.metro-u.ac.jp>
Secretariat:	Hiroshi Nakamura (Univ. of Tokyo, Japan)	<nakamura@hal.rcast.u-tokyo.ac.jp>

DSN Steering Committee

Chair:	Paulo E. Verissimo (Univ. of Lisbon, Portugal)	<Paulo.Verissimo@di.fc.ul.pt>
Vice Chair:	Jean Arlat (LAAS-CNRS, France)	<jean.arlat@laas.fr>
Communications Chair:	Charles B. Weinstock (SEI, USA)	<weinstock@sei.cmu.edu>
Members:	Tom Anderson (U. Newcastle, UK), Algirdas Avizienis (Vytautas Magnus U., Lithuania), Ram Chillarege (Chillarege Inc., USA), Elmootazbellah N. Elnozahy (IBM, Austin Research Lab., USA), W. Kent Fuchs (Cornell U., USA), Ravishankar K. Iyer (UIUC, USA), Johan Karlsson (Chalmers U., Sweden), Hermann Kopetz (T.U. Vienna, Austria), Jaynarayan Lala (Raytheon, USA) (TC-FTC Vice-Chair), Jean-Claude Laprie (LAAS-CNRS, France), Miroslaw Malek (Humboldt U., Germany), Roy Maxion (CMU, USA), Takashi Nanya (U. Tokyo, Japan), William H. Sanders (UIUC, USA), Richard D. Schlichting (AT&T Labs - Research, USA), Luca Simoncini (U. Pisa, Italy), Kishor Trivedi (Duke U., USA)	

Dependable Computing and Communications Symposium (DCCS)

Program Chair:

Andrea Bondavalli (Univ. of Florence, Italy) <a.bondavalli@dsi.unifi.it>

Program Committee to include:

Y. Amir, (Johns Hopkins, USA)
R. Baldoni (U. of Rome, Italy)
D. Blough (Georgia Tech, USA)
C. Cachin, (IBM, Switzerland)
A. Casimiro, (U. Lisbon, Portugal),
P. Coppola, (Intecs, Italy)
C. Costantinescu, (Intel, USA)
B. Cukic, (West Virginia U., USA)
M. Dacier, (Eurecom, France)
X. Defago, (JAIST, Japan)
F. Di Giandomenico, (ISTI-CNR, Italy)
E. Duarte, (U. of Paraná Curitiba, Brasil)
E.N. Elnozahy, (IBM Austin Research Lab., USA)

C. Fetzer, (TU Dresden, Germany)
M. Hiltunen, (AT&T Labs, USA)
V. Issarny, (INRIA, France)
F. Jahanian, (U. Michigan, USA)
R. Jimenez, (TU Madrid, Spain)
J. Kaiser, (U. of Ulm, Germany)
Y. Kakuda, (Hiroshima City U., Japan)
Z. Kalbarczyk, (UIUC, USA)
J. Karlsson, (Chalmers U., Sweden)
M.-O. Killijian, (LAAS-CNRS, France)
P. Koopman, (CMU, USA)
H. Kopetz, (TU Wien, Austria)
J. Lala, (Raytheon, USA)
C. Landwehr (U. of Maryland, USA)
M. Lyu, (Chinese U. of Hong Kong, Hong Kong)
R. Macedo (Federal U. of Bahia, Brasil)
I. Majzik, (Budapest UTE., Hungary)
K. Marzullo (UC San Diego, USA)

R. Maxion (CMU, USA)
I. Mura, (Motorola, Italy)
H. Nakamura, (U. of Tokyo, Japan)
L. Romano, (U. of Naples, Italy)
W. H. Sanders (UIUC, USA)
N. Saxena, (Stanford, USA)
A. Schiper (EPFL, Switzerland)
D. Siewiorek, (CMU, USA)
B. Smith, (IBM, USA)
N. Suri, (TU Darmstadt, Germany)
D. Taylor (U. of Waterloo, CA)
S. Upadhyaya, (SUNY at Buffalo, USA)
J. Xu, (U. Of Leeds, UK)
M. Zelha Rela, (U. of Coimbra, Portugal)

Ex Officio:

P. E. Verissimo and J. Arlat

Performance and Dependability Symposium (PDS)

Program Co-Chairs:

Boudewijn Haverkort (Univ. of Twente, Netherlands) <brh@cs.utwente.nl>

Dong Tang (Sun Microsystems, USA)
<dong.tang@sun.com>

Steering Committee:

R. K. Iyer (UIUC, USA) (**Chair**)
K. S. Trivedi (Duke U., USA) (**Co-Chair**)
J. Abraham (U. of Texas, Austin, USA)
A. Avizienis (Vytautas Magnus U., Lithuania)
G. Balbo (U. of Torino, Italy)
R. Chillarege (Chillarege, Inc., USA)
G. Haring (U. of Vienna, Austria)
R. Harper (IBM Research, USA)
J.-C. Laprie (LAAS-CNRS, France)
R. Marie (U. of Rennes, France)
E. J. McCluskey (Stanford U., USA)
W. H. Sanders (UIUC, USA)
M. Woodside (Carleton U., Canada)

Program Committee:

S. Bagchi (Purdue U., USA)
A. Bobbio (U. of Piemonte Orientale, Italy)
P. Buchholz (U. of Dortmund, Germany)
J. Cao (Hong Kong Polytec. U., Hong Kong)
J. Carrasco (U. of Catalunya, Spain)
R. Chillarege (Chillarege Inc., USA)
G. Ciardo (UC Riverside, USA)
D. Costa (Critical Software, Portugal)
M. Cukier (U. of Maryland, USA)
T. Dohi (Hiroshima University, Japan)
J. Dugan (U. of Virginia, USA)
K. Goseva-Popstojanova (W. Virginia U., USA)
R. Harper (IBM Research, USA)
G. Heijenk (U. of Twente, Netherlands)
Z. Kalbarczyk (UIUC, USA)
K. Kanoun (LAAS-CNRS, France)
J. Karlsson (Chalmers U., Sweden)
K. Keeton (HP Labs, USA)
Y. Kimura (Fujitsu, Japan)
S.-Y. Kuo (National Taiwan U., Taiwan)

I. Lee (Hanyang U., Korea)
Z. Li (Inst. of Computing Tech., China)
H. Madeira (U. of Coimbra, Portugal)
M. Massink (CNUCE, Italy)
R. Maxion (Carnegie Mellon U., USA)
V. Mendiratta (Lucent Bell Labs, USA)
A. van Moorsel (Consultant, Germany)
B. Murphy (Microsoft Research, UK)
N. Neves (U. of Lisbon, Portugal)
D. Nicol (UIUC, USA)
G. Rubino (IRISA, France)
W. H. Sanders (UIUC, USA)
M. Siegle (UBW Muenchen, Germany)
A. Somani (Iowa State U., USA)
A. Tai (IA Tech Inc., USA)
O. Torbjornsen (Sun Microsystems, Norway)
T. Tsai (Sun Microsystems, USA)
T. Tsuchiya (Osaka University, Japan)
J. Xu (NC State U., USA)

Ex Officio:

R.K.Iyer, K.S.Trivedi
and P. E. Verissimo

Dependable Computing and Communications Symposium (DCCS)

Program chair:
Andrea Bondavalli
<a.bondavalli@dsi.unifi.it>

Dipartimento di Sistemi e
Informatica,
Universita' di Firenze
/ISTI-CNR – Pisa

Via Lombroso, 6/17,
50134 Firenze, Italy

Tel: (+39) 055-4796776
Fax: (+39) 055-4796730

We invite original papers and other contributions reporting research and practical results covering all aspects of computer-system dependability, but very specially: resilient and dependable dynamic information networks and infrastructures, self-managing systems, survivable critical infrastructures, autonomic computing, intrusion detection and tolerance, the reliability aspects of human-computer interactions, dependable HW and SW architecture; distributed systems architectures and protocols.

Topic areas include, but are not limited to:

Adaptability	Multimedia Systems
Critical Infrastructures Protection	Quality of Service
Dependability in VLSI	Real-Time Systems
Distributed Systems	Safety-Critical Systems
E-commerce Dependability	Security Policy, Protection and Access Control
Fault Tolerance	Software and System Reliability
High-Speed Networks and Protocols	Software Testing, Validation, and Verification
Industrial Applications and Automotives	Storage Systems
Information Assurance and Survivability	Supercomputing and High-end Computing Systems
Intrusion Tolerance	System Dependability Modeling and Prediction
Internet Dependability	System Management and Self-managed Systems
Mobile Systems	Telecommunication Systems
	Transaction Processing

Information for Authors

Manuscripts in the following categories will be refereed and considered for publication in the Proceedings of the 2005 International Conference on Dependable Systems and Networks. Excessively long submissions, unoriginal work, or work that is submitted to multiple venues will be disregarded. Please submit in any of the following categories by *November 19, 2004*:

- Regular Papers describing recent research results.
- Practical Experience Reports describing an experience or a case study.
- Software and System Demonstrations describing the software or the system, its context and objective, and the planned demonstration. Optionally, authors may additionally send video recordings of their proposed demonstration.
- Panels, including the proposed topic(s), a description of the panel objectives, and the names and addresses of probable panelists.

The title page should include the title of the paper, author name(s), affiliations, mailing address, phone number, fax number, and e-mail address, a maximum 150-word abstract, five keywords (to help with referee assignment), the submission category (see above), an approximate word count, and a declaration that the material has been cleared through author affiliations. For multi-authored submissions, the contact author should be indicated.

The format of submissions for review should be single-column, Times 11-point size, double-spaced:

- 20 pages for Regular Papers (equivalent to 10 IEEE conference pages),
- 12 pages for Practical Experience Reports and Software and System demonstrations (equivalent to 6 IEEE conference pages),
- 4 pages (equivalent to 2 IEEE conference pages), plus biographical sketch for Panel Proposals.

The format for final, camera-ready submissions that have been accepted for publication should be in accordance with IEEE standard conference paper format.

Submit all materials through the web to <http://www.dsn.org> as Portable Document Format (.pdf) files. Please preview the manuscript in a viewer such as Acrobat to ensure its integrity before submitting it. Make sure you visually check uncommon fonts, symbols, equations, etc. A defective print can undermine your chances of success. For those who are unable to submit materials electronically or cannot generate .pdf files, please contact the program chair for special instructions.

Important Dates for DCCS

Submission: November 19, 2004

Notification: February 28, 2005

William C. Carter Award

The William C. Carter Award is presented annually since 1997 to recognize an individual who has made a significant contribution to the field of dependable computing through his or her graduate dissertation research. The award honors the late William C. Carter, a key figure in the formation and development of the field of dependable computing. The award is sponsored by IEEE Technical Committee on Fault-Tolerant Computing (TC-FTC) and IFIP Working Group on Dependable Computing and Fault Tolerance (WG 10.4). To qualify, a paper based on the student's dissertation must have been submitted to the DCC Symposium within DSN as a regular paper with the student as the first author. Both current and former graduate students, no more than two years past completion of their dissertations, are eligible. A dissertation advisor wishing to nominate a student should submit the nomination form which can be found through the conference website at www.dsn.org. All Carter Award submissions accepted as regular papers to the DCC Symposium are evaluated by the Steering Committee of the Conference.

Performance and Dependability Symposium (PDS)

Our economies and our daily lives become increasingly dependent on networked information systems, either explicitly, or, more and more often, invisibly, via networked embedded systems. The healthy operation of all these systems, in terms of their performance and dependability (and their interaction), is becoming crucially important for us. Therefore, analysts, designers, and system administrators must consider the relationships between the occurrence of faults and their impact on performance and on degraded service modes, both in computer systems and in communication networks interconnecting these systems. The Performance and Dependability Symposium (PDS) aims to bring together academic and industrial researchers in these two areas, with emphasis on integrating theory and practice. Relating analytical techniques to simulations, actual measurements, and experiments will be the broad theme of the symposium. Both hardware and software performance and dependability issues in designing and maintaining computer systems and networks will be the topics of interest.

Program Co-Chairs:

Boudewijn Haverkort
<brh@cs.utwente.nl>
Univ. of Twente, Netherlands
Faculty for Electrical Engineering,
Mathematics and Computer Science
P.O. Box 217, 7500 AE Enschede,
the Netherlands.
Tel: (+31) 53-4898041
Fax: (+31) 53-4894524

Dong Tang
<dong.tang@sun.com>
Sun Microsystems
MS UNWK20-310
7788 Gateway Blvd, Building 20
Newark, CA 94560, USA
Tel: (+1) 408-404-7081
Fax: (+1) 408-404-7081

We invite original papers and other contributions reporting research and practical results covering all aspects of analytical, simulation, testing and measurement techniques for performance and dependability applied to:

- Fault-Tolerant and Self-Healing Systems
- Distributed Systems and Networks
- Parallel and Clustered Systems
- Real-Time and Embedded Systems
- Database and Transactional Systems
- Secure and Intrusion Tolerant Systems

Regular papers, practical experience reports, tool descriptions/demonstrations, and panel proposals are especially welcome in the following topics:

- Analytical, numerical and simulation techniques for performance and dependability assessment
- Measurement techniques for performance and dependability assessment
- Dependability benchmarking and fault injection/robustness testing
- Design and use of tools for performance and dependability assessment
- Model-checking of timed and stochastic systems
- Operational data for analysis of performance and dependability
- Performance and dependability issues in managing large systems and networks
- Case studies on performance and dependability issues in designing computer and communication systems
- Transaction recovery and its impact on performance and dependability
- Modeling and measurement of cyber security

Information for Authors

Manuscripts in the following categories will be refereed and considered for publication in the IEEE Proceedings of the 2005 International Conference on Dependable Systems and Networks (excessively long submissions, unoriginal work, or work that is submitted to multiple venues will be disregarded).

- Regular Papers describing original research
- Practical Experience Reports describing an experience or case study
- Tool Descriptions/Demonstrations
- Panels, including the proposed topic(s), a description of the panel objectives, and the names and addresses of probable panelists

The title page should include the title of the paper, author names, affiliations, mailing address, phone/fax numbers, and email address, a maximum 150-word abstract, five keywords, the submission category (see above), an approximate word count, and a declaration that the material has been cleared through the author affiliations. For multi-authored submissions, the contact author should be indicated. The format of submissions for review should be single-column, Times or Times Roman 11-point size, double-spaced:

- 20 pages for Regular Papers (equivalent to 10 IEEE conference pages)
- 12 pages for Practical Experience Reports and Tool Descriptions/Demonstrations (equivalent to 6 IEEE conference pages),
- 4 pages (equivalent to 2 IEEE conference pages) plus biographical sketch, for Panel Proposals.

The format for final, camera-ready submissions that have been accepted for publication should be in accordance with the IEEE standard conference paper format. Submit all materials through the web via <http://www.dsn.org/> as PostScript (.ps) or Portable Document Format (.pdf) files. Before the submission, please preview the manuscript in a viewer such as GhostView or Acrobat to ensure its integrity. For those who are unable to submit materials electronically, please contact the program co-chairs for special instructions.

Important Dates for PDS

Submission: November 19, 2004

Notification: February 28, 2005

Workshops

Chair

Nuno Ferreira Neves
(Univ. of Lisbon, Portugal)
<nuno@di.fc.ul.pt>

Program Committee

Lorenzo Alvisi
(U. of Texas, Austin, USA)
Patrick Eugster
(Sun Microsystems, Switzerland)
Raimundo Macedo
(Federal U. of Bahia, Brasil)
David Powell
(LAAS-CNRS, France)

Deadlines

Submission: October 8, 2004
Notification: November 12, 2004

Workshops provide a forum for a group of (typically 25 to 40) participants to exchange opinions on topics related to any of the facets of dependable systems and networks. We welcome participation by professionals with diverse backgrounds who can contribute to advancing the technology and understanding of the workshop subject. Workshops are not alternative forums for full papers, which should be submitted to one of the two DSN symposia. Workshop attendance is open to all registered conference attendees. Each workshop may be a half day or one day in length.

Proposals for workshops are solicited. A workshop submission consists of a three-page proposal containing:

- the title of the workshop;
- an outline of the theme, goals, and main challenges of the workshop;
- a description of the desired number of participants, the participant solicitation and selection process, pre-workshop activities, planned workshop activities including length of time and required rooms, and expected workshop outputs;
- a brief description of each organizer's background, including relevant past experience with workshops.

Acceptance will be based on an evaluation of the workshop's potential for generating useful results, the timeliness and expected interest in the topic, and the proposers' ability to lead a successful workshop.

Accepted workshops will have a 2-page proceedings-format summary produced by the organizers of each workshop in the Main Proceedings of the conference. Workshop contributions (summaries, short papers, etc.) will be included in the Supplement of the DSN Proceedings.

Please submit all workshop proposals through the web to <http://www.dsn.org/> as Portable Document Format (.pdf), or Post Script (.ps) files.

Proposers of workshops will be notified by November 12, 2004

Details and deadlines for submissions to accepted workshops will be posted after November 13 on the DSN web site and elsewhere.

Tutorials

Chair

Zbigniew Kalbarczyk
(Univ. of Illinois, USA)
<kalbar@crhc.uiuc.edu>

Program Committee

Armando Fox
(Stanford Univ., USA)
Johan Karlsson
(Chalmers Univ. of Technology, Sweden)
Henrique Madeira
(Univ. of Coimbra, Portugal)
Priya Narasimhan
(Carnegie Mellon Univ., USA)
William H. Sanders
(Univ. of Illinois, USA)
Lawrence Votta
(Sun Microsystems, USA)

Deadlines

Submission: January 14, 2005
Notification: February 28, 2005

Proposals are solicited for tutorials on topics that cover all aspects of dependability. Candidate topics include, but are not limited to:

- Theory and practice of dependable system design
- Hardware and software dependability techniques
- Dependability assessment
- Risk assessment
- Field data acquisitions and analysis
- Dependable user interfaces
- Dependability of requirements and/or specifications
- Other important emerging technical areas

Tutorials about real-world systems in which the instructional material regards what actually happens in product and system development, as opposed to what happens in the research laboratory, are particularly welcome. Also encouraged are demonstrations of how techniques and tools from one area of dependability can be applied to others, such as fault tolerance to information assurance, safety, autonomous systems or autonomous diagnosis.

Tutorials should present a well-defined area with sufficient background, motivation, clarity and depth to enable the attendees to acquire a basic familiarity with the topic. Appropriate supporting materials should accompany presentations. Tutorial submissions are limited to 5 pages and should include the following:

- Description of the topic
- Structure of the tutorial
- Expected background of the audience
- Proposed supporting materials
- Brief resume or biographical sketch of the presenter

Please submit all contributions through the web to <http://www.dsn.org/> as Portable Document Format (.pdf), or Post Script (.ps) files.

Student Forum

Chair

Philip Koopman
(Carnegie Mellon Univ., USA)
<koopman@cmu.edu>

Program Committee

Henrique Madeira
(Univ. Coimbra, Portugal)
Kazuhiko Iwasaki
(Tokyo Metro. Univ., Japan)

Deadlines

Submission: April 1, 2005
Notification: April 21, 2005

The Student Forum will provide an opportunity for students working in the area of dependable computing to present and discuss their research objectives, approaches and preliminary results. The Forum is centered around a conference track during which the selected "student research papers" will be presented.

Preparation and Submission: Student research papers should be single-authored and describe preliminary results and future directions of the student's on-going research. The papers will be reviewed by a committee led by the Student Forum Chair. Accepted contributions will be published in the Supplement of the 2005 International Conference on Dependable Systems and Networks.

Papers should be no more than three pages long (IEEE double-column format), and should be submitted in camera-ready form, ready to be printed. There is no revision cycle. Contributions should be submitted through the web to <http://www.dsn.org/> as Portable Document Format (.pdf), or Post Script (.ps) files.

Acceptance and Notification: The criteria for acceptance of Student Research Papers will be originality, relevance and interest to the community. You will be notified by April 21, 2005.

Important Notes: The final submission deadline is April 1, 2005, but you are encouraged to send in your submission well in advance of the final deadline. Late submissions will not be accepted; no fooling. By submitting a Student Research Paper you are committing yourself to present it at the conference if it is accepted.

Questions or comments about the Student Forum should be directed to the Student Forum Chair: Philip Koopman (Carnegie Mellon Univ., USA) koopman@cmu.edu

Student Travel Grants

The IEEE Computer Society Technical Committee on Fault-Tolerant Computing will solicit applications for travel scholarships to partially offset the cost of students planning to attend DSN-2005 in Yokohama, Japan, June 28 - July 1, 2005.

To be eligible, the applicant must be enrolled in a degree program at the time of DSN-2005. Authors of accepted papers, students submitting papers to the Student Forum, and others are all welcome to apply. Participation, such as presenting a regular paper or a paper in the Student Forum will be used as one criterion in awarding the scholarships, however. The Technical Committee anticipates awarding scholarships in the range of \$500.

To apply, submit a write-up not exceeding 500 words stating your student status and summarizing your current research related to dependable computing and describing how it is relevant to DSN-2005, what you hope to learn by attending the conference, and how you expect it to affect your future research directions. Also include your name, postal address, electronic mail address, telephone number, and fax number. Applications must be received no later than April 1, 2005 and should be submitted through the web to <http://www.dsn.org/> as Portable Document Format (.pdf), or Post Script (.ps) files. The awards will be announced by April 21, 2005. Questions about the Student Grant applications should be directed to the Student Forum Chair: Philip Koopman (Carnegie Mellon Univ., USA) koopman@cmu.edu.

Fast Abstracts

Chair

Matti Hiltunen
(AT&T Labs - Research, NJ, USA)
<hiltunen@research.att.com>

Program Committee

Antonio Casimiro Costa
(University of Lisbon)
David Bakken
(Washington State University)

Deadlines

Submission: April 1, 2005
Notification: April 21, 2005

Fast Abstracts are lightly refereed short presentations of work in progress or opinion pieces that can cover any and all facets of dependable systems and networks. Because they are brief and have a later deadline, Fast Abstracts allow their authors to:

- Report on work that may or may not be complete.
- Introduce new ideas to the community.
- State positions on controversial issues.

As such, they provide an excellent opportunity to present new work and receive early feedback from the community. We particularly encourage submission from conference participants that would not otherwise present their work at the conference.

Submissions should be at most two pages, formatted using standard two-column IEEE format. They are refereed primarily based on their relevance to the conference. Accepted Fast Abstracts will be published in the Supplement of the 2005 International Conference on Dependable Systems and Networks.

All participants in this track will have an opportunity to present a poster; selected participants will also present a 5 minute talk, including 1 minute for questions. Full submission information is available from the conference website (www.dsn.org).



**The International Conference on
Dependable Systems and Networks
c/o Takashi Nanya
RCAST, the University of Tokyo
4-6-1 Komaba, Meguro-ku, Tokyo
153-8904 JAPAN**