

The International Conference on
**Dependable
Systems and
Networks**



Yokohama, Japan
June 28 – July 1, 2005

The International Conference on Dependable Systems and Networks 2005

PACIFICO YOKOHAMA Conference Center, Yokohama, Japan
June 28 – July 1, 2005

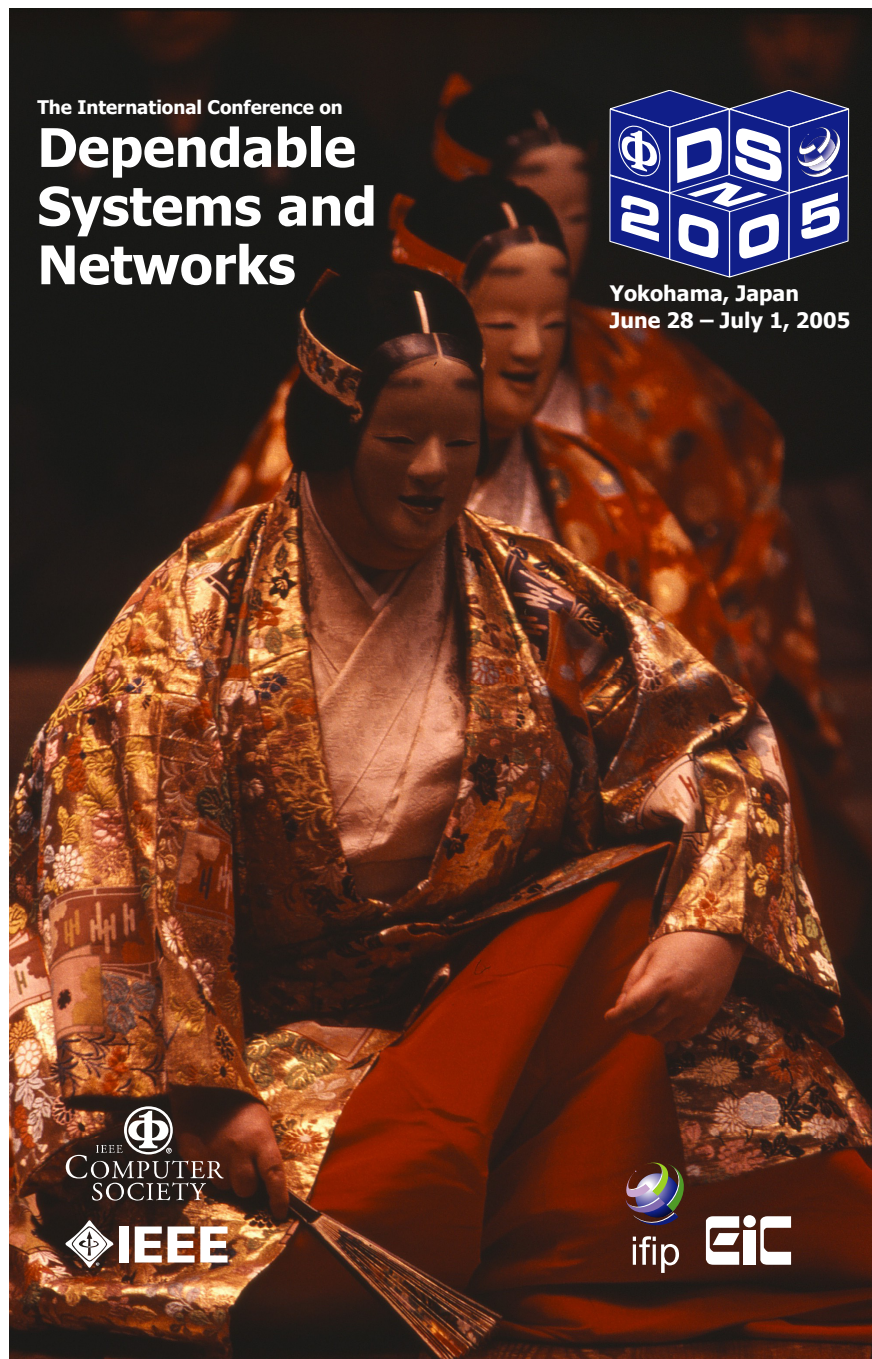
Final Program

Sponsored by: IEEE Computer Society Technical Committee on Fault-Tolerant Computing
IFIP WG 10.4 on Dependable Computing and Fault Tolerance
IEICE Technical Group on Dependable Computing

In cooperation with: University of Tokyo, Japan, Osaka University, Japan, University of Florence, Italy,
University of Twente, the Netherlands, Sun Microsystems, USA

June 28 (Tue)

08:30	Tutorial Registration	
09:00	< Room : 311+312 >	
09:00	Tutorial A <i>Mobile Ad Hoc Networks : Protocols and Security Issues</i> Nitin Vaidya (University of Illinois at Urbana-Champaign, USA)	
12:30	Lunch	
12:30		
13:30	< Room : 311+312 > Tutorial C <i>Hands-On Experiences with the SAE Standard Architecture Analysis & Design Language (AADL) in High Dependability Design</i> David Gluch (Embry-Riddle Aeronautical U., USA), Peter Feiler (SEI, CMU, USA), Bruce Lewis (Army AMCOM SED, USA)	< Room : 313+314 > Tutorial E <i>Reliable Distributed Programming</i> Rachid Guerraoui (EPFL, Switzerland), Luis Rodrigues (U. Lisboa, Portugal)
17:00	< Room : Lounge >	
18:00	Welcome Reception / DSN Registration	
20:00		



June 29 (Wed) – Morning

08:00	DSN Registration				
08:30	< Room : 301+302 >				
	Opening Remarks				
	< Room : 301+302 >				
	Keynote Address				
	<i>Observation of Local and Distant Earthquakes and Tsunami Warning System</i> Mitsuyuki Hoshiba (Japan Meteorological Agency, Japan)				
10:00					
10:00	Coffee Break				
10:30	< Room : 303 >	< Room : 301+302 >	< Room : 304 >	< Room : 311+312 >	< Room : 313+314 >
	Session 1A: Fault Tolerant Architectures and Algorithms Chair : Jean-Claude Laprie (LAAS-CNRS, France) <i>Assured Reconfiguration of Fail-Stop Systems</i> E. A. Strunk, J. C. Knight, and M. A. Aiello (University of Virginia, USA) <i>NonStop® Advanced Architecture</i> D. Bernick, B. Bruckert, P. Del Vigna, D. Garcia, R. Jardine, J. Klecka, and J. Smullen (HP, USA) <i>How Fast Can Eventual Synchrony Lead to Consensus?</i> P. Dutta, R. Guerraoui (Ecole Polytechnique Fédérale de Lausanne, Switzerland), and L. Lamport (Microsoft Corporation, USA)	Session 1B: Dependability in VLSI Chair : Cristian Constantinescu (Intel, USA) <i>ReStore: Symptom Based Soft Error Detection in Microprocessors</i> N. J. Wang and S. J. Patel (University of Illinois Urbana-Champaign, USA) <i>Combining Error Masking and Error Detection Plus Recovery to Combat Soft Errors in Static CMOS Circuits</i> S. Krishnamohan and N. Mahapatra (Michigan State University, USA) <i>On-line Detection of Control-Flow Errors in SoCs by Means of an Infrastructure IP Core</i> P. Bernardi (Politecnico di Torino, Italy), L. M. Bolzani (Pontificia Universidade Católica do Rio Grande do Sul, Brazil), M. Rebaudengo, M. Sonza Reorda, M. Violante (Politecnico di Torino, Italy), and F.L. Vargas (Pontificia Universidade Católica do Rio Grande do Sul, Brazil)	Session 1C: Software Reliability, Rejuvenation and Optimization Chair : Karama Kanoun (LAAS-CNRS, France) <i>Combining Response Surface Methodology with Numerical Models for Optimization of Class-Based Queueing Systems</i> P. Kemper, D. Müller, and A. Thümmel (Universität Dortmund, Germany) <i>On a Method for Mending Time to Failure Distributions</i> M. Grottke and K. S. Trivedi (Duke University, USA) <i>A Performability-Oriented Software Rejuvenation Framework for Distributed Applications</i> A. T. Tai, K. S. Tso (IA Tech, Inc., USA), W. H. Sanders (University of Illinois, USA), and S. N. Chau (Jet Propulsion Laboratory, USA)	Workshop 1: Dependable Software - Tools and Methods [Systems and Tools] Chair : Takuo Watanabe (Tokyo Institute of Technology, Japan) <i>Difference of Degradation Schemes among Operating Systems – Experimental analysis for web application servers –</i> Hideaki Hibino, Kenichi Kourai, and Shigeru Chiba (Tokyo Institute of Technology, Japan) (*) <i>XGE-ProtoDevel: A Communication Protocol Development Tool for 10Gbps Class Network</i> Shinji Sumimoto, Mitsuru Sato, Kohta Nakashima, and Kouichi Kumon (Fujitsu Laboratories Ltd., Japan), Yutaka Ishikawa (University of Tokyo, Japan) (*) <i>A Methodology for Designing Fault Injection Experiments as an Addition to Communication Systems Conformance Testing</i> Ana Maria Ambrosio, Fátima Mattiello-Francisco, N. L. Vijaykumar, S. V. de Carvalho, Valdivino Santiago (National Institute for Space Research, Brazil), and Eliane Martins (Campinas State University, Brazil) (*) <i>A Consistency Checker for UML Model Diagrams</i> Yasser Kotb and Takuya Katayama (Japan Advanced Institute of Science and Technology, Japan) The short presentations, 20 minute talks, are marked as (*).	Industry Session [I] Chair : Tohru Kikuno (Osaka University, Japan) <i>Fujitsu PRIMEQUEST: 32-way SMP Open Servers with Powerful Reliability Features</i> Toshiyuki Shimizu, Yasuhide Shibata, Haruhiko Ueno, Takumi Takeno, Shinya Kato, Seishi Okada, Nobuo Uchida, Hiroyuki Adachi, Hideki Maeda, Takato Noda, Akira Kabemoto (Fujitsu Limited, Japan) <i>Multi-Tier Checkpointing for Peta-Scale Systems</i> Alan Wood, Swami Nathan, Timothy Tsai, Chris Vick, Lawrence Votta (Sun Microsystems, USA), Anoop Vetteth (University of Illinois, USA) <i>The Construction of Dependable Network as Social Infrastructure</i> Akira Arutaki, Toshiyuki Kanoh, Takashi Egawa, Yoshihide Kikuchi, Yoshiaki Kiriha, Atsushi Iwata (NEC Corporation, Japan)
12:00					
12:00	< Room : Pacific >			< Room : Lounge >	
13:00	Lunch			Lunch (Steering Committee Meeting)	

June 29 (Wed) – Afternoon

13:00	< Room : 303 > Session 2A: Experimental Validation Chair : Ira Pramanick (Sun Microsystems, USA) <i>User Interface Dependability through Goal-Error Prevention</i> R. W. Reeder and R. A. Maxion (Carnegie Mellon University, USA) <i>Stability Monitoring and Analysis of Learning in an Adaptive System</i> S. Yerramalla, M. Mladenovski, B. Cukic, and E. Fuller (West Virginia University, USA) <i>Effective Testing and Debugging Techniques for a Group Communication System</i> E. Farchi (IBM Research, Israel), G. Kliot (Technion, Israel), Y. Krasny, A. Krits, and R. Vitenberg (IBM Research, Israel) <i>Error Propagation Profiling of Operating Systems</i> A. Johansson and N. Suri (TU-Darmstadt, Germany)	< Room : 301+302 > Session 2B: System Security Chair : Guenter Heiner <i>How Resilient are Distributed f Fault/Intrusion-Tolerant Systems?</i> P. Sousa, N. F. Neves, and P. Verissimo (University of Lisboa, Portugal) <i>Towards a Theory of Insider Threat Assessment</i> R. Chinchani, A. Iyer, H. Q. Ngo, and S. Upadhyaya (University at Buffalo, SUNY, USA) <i>Constructing Multi-Layered Boundary to Defend Against Intrusive Anomalies: An Autonomic Detection Coordinator</i> Z. Zhang and H. Shen (Japan Advanced Institute of Science and Technology, Japan) <i>A Model of Stateful Firewalls and its Properties</i> M. G. Gouda and A. X. Liu (The University of Texas at Austin, USA)	< Room : 304 > Session 2C: Security Evaluation Chair : Jun Xu (North Carolina State University, USA) <i>Optimizing the Pulsing Denial-of-Service Attacks</i> X. Luo and R. K. C. Chang (The Hong Kong Polytechnic University, China) <i>Towards a Security Benchmark for Database Management Systems</i> M. Vieira (ISEC/CISUC - Polytechnic Institute of Coimbra, Portugal) and H. Madeira (DEI/CISUC - University of Coimbra, Portugal) <i>An Experimental Evaluation to Determine if Port Scans are Precursors to an Attack</i> S. Panjwani, S. Tan, K. M. Jarrin, and M. Cukier (University of Maryland, USA) <i>LITEWOP: A Lightweight Countermeasure for the Wormhole Attack in Multihop Wireless Networks</i> I. Khalil, S. Bagchi, and N. Shroff (Purdue University, USA)	< Room : 311+312 > Workshop 1 (continued) [Invited Talk] Chair : Takuya Katayama (Japan Advanced Institute of Science and Technology, Japan) <i>Building Foundations for Dependable Systems</i> Richard D. Schlichting (AT&T Labs-Research, USA) [Concurrency and Tools] Chair : Yoshiki Kinoshita (National Institute of Advanced Industrial Science and Technology, Japan) <i>Typing for Reliable Distributed Systems - Recent Advances</i> Pawel T. Wojciechowski (Ecole Polytechnique Fédérale de Lausanne, Switzerland) <i>Congruences Properties for a Timed Extension of the π Calculus</i> Hiroaki Kuwabara, Shoji Yuen, and Kiyoshi Agusa (Nagoya University, Japan) (*)<i>Model Checking of Multi-Process Applications Using SBUML and GDB</i> Yoshihito Nakagawa (University of Tokyo, Japan), Richard Potter (Japan Science and Technology Agency, Japan), Mitsuharu Yamamoto (Chiba University, Japan), Masami Hagiya (University of Tokyo, Japan), and Kazuhiko Kato (Tsukuba University, Japan)	< Room : 313+314 > Student Forum Chair : Philip Koopman (Carnegie Mellon University, USA) <i>Dependability Analysis of the Java Virtual Machine</i> Salvatore Orlando (University of Naples Federico II, Italy) <i>Crash-Time Checkpoint and Recovery System</i> Zhigang Huo (Graduate School of Chinese Academy of Sciences, China) <i>Towards Adaptive Process Ordering in Asymmetric Distributed Protocols</i> Livia Sampaio (Federal University of Campina Grande, Brazil) <i>Proactive System Diagnosis Based on a Metastasis Model</i> Seung Gu Kim (Pohang University of Science and Technology, Korea)
15:00	Coffee Break				
15:00					
15:30					
15:30	< Room : 303 > Session 3A: Multicast Chair : Ricardo Jiménez Peris (TU Madrid, Spain) <i>GoCast: Gossip-enhanced Overlay Multicast for Fast and Dependable Group Communication</i> C. Tang, R. N. Chang, and C. Ward (IBM, USA) <i>SMRP: Fast Restoration of Multicast Sessions from Persistent Failures</i> J. Wu and Kang G. Shin (University of Michigan, USA) <i>Efficient Byzantine Broadcast in Wireless Ad-Hoc Networks</i> V. Drabkin, R. Friedman, and M. Segal (Technion, Israel)	< Room : 301+302 > Session 3B: Wide Area Networks Chair : Christof Fetzer (TU Dresden, Germany) <i>Internet Routing Anomaly Detection and Visualization</i> T. Wong, V. Jacobson, and C. Alaettinoglu (Packet Design Inc, USA) <i>Assessing the Performance of Erasure Codes in the Wide-Area</i> R. L. Collins and J. S. Plank (University of Tennessee - Knoxville, USA) <i>Finding Critical Traffic Matrices</i> Y. Zhang and Z. Ge (AT&T Labs-Research, USA)	< Room : 304 > Session 3C: Evaluation of QoS and Self-Healing Systems Chair : Michel Cukier (University of Maryland, USA) <i>Experimental Evaluation of the QoS of Failure Detectors on Wide Area Network</i> L. Falai and A. Bondavalli (University of Florence, Italy) <i>Probabilistic QoS Guarantees for Supercomputing Systems</i> A. J. Oliner, L. Rudolph (Massachusetts Institute of Technology, USA), R. K. Sahoo, J. E. Moreira, and M. Gupta (IBM TJ Watson Research Center, USA) <i>Ensembles of Models for Automated Diagnosis of System Performance Problems</i> S. Zhang (Stanford University, USA), I. Cohen, M. Goldszmidt, J. Symons (HP-Labs, USA), and A. Fox (Stanford University, USA)	< Room : 311+312 > Workshop 1 (continued) [Models] Chair : Shoji Yuen (Nagoya University, Japan) <i>Execution Monitoring and Information Flow Properties</i> Naoyuki Nagatou and Takuo Watanabe (Tokyo Institute of Technology, Japan) <i>Preconditions of Properties Described in CTL for Statements Manipulating Pointers</i> Yoshinori Tanabe, Toshinori Takai, Toshifusa Sekizawa (National Institute of Advanced Industrial Science and Technology, Japan Science and Technology Agency, Japan) and Koichi Takahashi (National Institute of Advanced Industrial Science and Technology, Japan) (*)<i>Analyzing Behaviors in VDM Specifications by Focusing on User-defined Types</i> Kengo Miyoshi, Satoru Hirachi, Shigeru Kusakabe, and Keiji Araki (Kyushu University, Japan)	< Room : 313+314 > Fast Abstracts [I] (The titles of the presentations are listed in P.8)
17:00					

June 30 (Thu) – Morning

08:00 08:30	DSN Registration				
08:30	< Room : 303 > Session 4A: Detection and Adaptation Chair : Jie Xu (University of Leeds, UK) <i>A Distributed State Monitoring Service for Adaptive Application Management</i> P. Murray (Hewlett-Packard Laboratories, UK) <i>Definition and Specification of Accrual Failure Detectors</i> X. Défago, P. Urbán, N. Hayashibara, and T. Katayama (Japan Advanced Institute of Science and Technology, Japan) <i>The Effects of Algorithmic Diversity on Anomaly Detector Performance</i> K. Tan and R. A. Maxion (Carnegie Mellon University, USA)	< Room : 301 > Session 4B: FT Communications Chair : Bojan Cukic (West Virginia University, USA) <i>On Partial Protection in Groomed Optical WDM Mesh Networks</i> J. Fang (Iowa State University, USA), M. Sivakumar (University of Maryland, Baltimore County, USA), A. K. Somani (Iowa State University, USA), and K. M. Sivalingam (University of Maryland, Baltimore County, USA) <i>Resilient Routing Layers for Recovery in Packet Networks</i> A. F. Hansen (Simula Research Laboratory and Telenor R, D, Norway), T. Cicic, S. Gjessing, A. Kvalbein, and O. Lysne (Simula Research Laboratory, Norway) <i>Perturbation-Resistant and Overlay-Independent Resource Discovery</i> S. Y. Ko and I. Gupta (University of Illinois at Urbana-Champaign, USA)	< Room : 304 > Session 4C: Experimental Evaluation of Fault-Tolerance Chair : João Gabriel Silva (University of Coimbra, Portugal) <i>A Framework for Node-Level Fault Tolerance in Distributed Real-time Systems</i> J. Aidemark, P. Folkesson, and J. Karlsson (Chalmers University of Technology, Sweden) <i>Experimental Dependability Evaluation of a Fail-Bounded Jet Engine Control System for Unmanned Aerial Vehicles</i> J. Vinter (Chalmers University of Technology, Sweden), O. Hannius, T. Norlander (Volvo Aero Corporation, Sweden), P. Folkesson, and J. Karlsson (Chalmers University of Technology, Sweden) <i>TIBFIT: Trust Index Based Fault Tolerance for Arbitrary Data Faults in Sensor Networks</i> M. Krasniewski, P. Varadharajan, B. Rabeler, S. Bagchi, and Y. C. Hu (Purdue University, USA)	< Room : 311+312 > Workshop 2: Hot Topics in System Dependability [Introduction] [Distributed Systems] Chair : Kimberly Keeton (Hewlett Packard Labs, USA) <i>The Virtue of Dependent Failures in Multi-Site Systems</i> Flavio P. Junqueira, Keith Marzullo (University of California, San Diego, USA) <i>A Root-Cause Localization Model for Large-Scale Systems</i> Emre Kiciman (Stanford University, USA), Lakshminarayanan Subramanian (University of California, Berkeley, USA) <i>The Role of Accountability in Dependable Distributed Systems</i> Aydan R. Yumerefendi, Jeffrey S. Chase (Duke University, USA) <i>Q&A Mini-panel</i>: Flavio Junqueira, Emre Kiciman, Aydan Yumerefendi	< Room : 313+314 > Fast Abstracts [II] (The titles of the presentations are listed in P.8)
10:00 10:00 10:30	Coffee Break				
10:30	< Room : 303 > Session 5A: Operating Systems and Mechanisms Chair : Neeraj Suri (TU-Darmstadt, Germany) <i>Cruz: Application-Transparent Distributed Checkpoint-Restart on Standard Operating Systems</i> G. J. Janakiraman, J. R. Santos, D. Subhraveti, and Y. Turner (HP Laboratories, USA) <i>A Multi-Level Meta-Object Protocol for Fault-Tolerance in Complex Architectures</i> F. Taïani, J.-C. Fabre, and M.-O. Killijian (LAAS-CNRS, France) <i>Crash Data Collection: A Windows Case Study</i> A. Ganapathi and D. Patterson (Univerisy of California, Berkeley, USA)	< Room : 301 > Session 5B: Networking Chair : Elias P. Duarte, Jr. (Federal U. of Paraná, Brazil) <i>Scalable and Robust WLAN Connectivity Using Access Point Array</i> F. Guo and T.-c. Chiueh (Stony Brook University, USA) <i>Ringin out Fault Tolerance. A New Ring Network for Superior Low-Cost Dependability</i> B. Hall, K. Driscoll, M. Paulitsch, and S. Dajani-Brown (Honeywell, USA) <i>A System Demonstration of ST-TCP</i> M. Marwah, S. Mishra (University of Colorado at Boulder, USA), and C. Fetzer (TU Dresden, Germany)	< Room : 304 > Session 5C: Performance Evaluation of Networks and Protocols Chair : Sy-Yen Kuo (National Taiwan University, Taiwan) <i>Improving TCP Performance for Multihop Wireless Networks</i> S. M. ElRakabawy, C. Lindemann (University of Dortmund, Germany), and M. Vernon (University of Wisconsin, USA) <i>A Spatial Fluid-based Framework to Analyze Large-Scale Wireless Sensor Networks</i> M. Gribaudo (Universita'di Torino, Italy), C.-F. Chiasserini (Politecnico di Torino, Italy), R. Gaeta (Universita' di Torino, Italy), M. Garetto (Politecnico di Torino, Italy), D. Manini, and M. Sereno (Universita' di Torino, Italy) <i>Are You Still There? – A Lightweight Algorithm to Monitor Node Presence in Self-Configuring Networks</i> H. Bohnenkamp, J. Gorter (Univ. of Twente, the Netherlands), J. Guidi (Philips Research, the Netherlands), and J.-P. Katoen (Univ. of Twente, the Netherlands, Aachen Univ., Germany)	< Room : 311+312 > Workshop 2 (continued) [Dependable Services] Chair : Angelos Keromytis (Columbia University, USA) <i>Trusted Virtual Domains: Toward Secure Distributed Services</i> John Linwood Griffin, Trent Jaeger, Ronald Perez, Reiner Sailer, Leendert van Doorn, Ramón Cáceres (IBM Research, USA) <i>Computational Risk Management for Building Highly Reliable Network Services</i> Brent N. Chun, Philip Buonadonna (Intel Research, Berkeley, USA), Chaki Ng (Harvard Unviersity, USA) <i>On the Challenge of Delivering High-Performance, Dependable, Model-Checked Internet Servers</i> Anil Madhavapeddy (University of Cambridge, UK), David Scott (Fraser Research) <i>Q&A Mini-panel</i>: Brent Chun, John Linwood Griffin, Anil Madhavapeddy	< Room : 313+314 > Fast Abstracts [III] (The titles of the presentations are listed in P.9)
12:00					

June 30 (Thu) – Afternoon

12:00	< Room : Pacific > Lunch			< Room : Lounge > Lunch (2005 PC Final Meeting)	
13:00	< Room : 303 >	< Room : 301 >	< Room : 304 >	< Room : 311+312 >	< Room : 302 >
13:00	Session 6A: Codes Chair : David Taylor (University of Waterloo, Canada) <i>A Data-Centric Approach to Checksum Reuse for Array-Intensive Applications</i> G. Chen, M. Kandemir, and M. Karakoy (The Pennsylvania State University, USA) <i>Small Parity-Check Erasure Codes – Exploration and Observations</i> J. S. Plank, A. L. Buchsbaum (AT&T Labs-Research, USA), R. L. Collins, and M. G. Thomason (University of Tennessee, USA) <i>Using Erasure Codes Efficiently for Storage in a Distributed System</i> M. K. Aguilera (HP Labs, USA), R. Janakiraman, and L. Xu (Washington University in St. Louis, USA) <i>Coverage and the Use of Cyclic Redundancy Codes in Ultra-Dependable Systems</i> M. Paulitsch (Honeywell Laboratories, USA), J. Morris (Carnegie Mellon University, USA), B. Hall, K. Driscoll (Honeywell Laboratories, USA), E. Latronico, and P. Koopman (Carnegie Mellon University, USA)	Session 6B: Critical Infrastructures Protection Chair : Shambhu Upadhyaya (SUNY at Buffalo, USA) <i>Authenticated System Calls</i> M. Rajagopalan, M. Hiltunen (University of Arizona, USA), T. Jim, and R. Schlichting (AT&T Labs-Research, USA) <i>Detecting Stealth Software with Strider GhostBuster</i> Y.-M. Wang, D. Beck (Microsoft Research, USA), B. Vo (Massachusetts Institute of Technology, USA), R. Roussev (Florida Institute of Technology, USA), and C. Verbowski (Microsoft Research, USA) <i>Defeating Memory Corruption Attacks via Pointer Taintedness Detection</i> S. Chen (University of Illinois at Urbana-Champaign, USA), J. Xu (North Carolina State University, USA), N. Nakka, Z. Kalbarczyk, and R. K. Iyer (University of Illinois at Urbana-Champaign, USA) <i>Checking Array Bound Violation Using Segmentation Hardware</i> L.-c. Lam and T.-c. Chiueh (Stony Brook University, USA)	Session 6C: Markovian Models for Performance and Dependability Chair : Boudewijn Haverkort (University of Twente, Netherlands) <i>A Novel Approach for Fitting Probability Distributions to Real Trace Data with the EM Algorithm</i> A. Thümmel, P. Buchholz (University of Dortmund, Germany), and M. Telek (Budapest University of Tech. and Econ. Hungary) <i>Model Checking Markov Reward Models with Impulse Rewards</i> L. Cloth, J.-P. Katoen, M. Khattri (University of Twente, the Netherlands), and R. Pulungan (Saarland University, Germany) <i>A Wavefront Parallelisation of CTMC Solution Using MTBDDs</i> Y. Zhang, D. Parker, and M. Kwiatkowska (University of Birmingham, UK) <i>Lumping Matrix Diagram Representations of Markov Models</i> S. Derisavi (University of Illinois at Urbana-Champaign, USA), P. Kemper (Dortmund University, Germany), and W. H. Sanders (University of Illinois at Urbana-Champaign, USA)	Workshop 2 (continued) [Challenge Your Assumptions] Chair : Priya Narasimhan (Carnegie Mellon University, USA) <i>Why Traditional Storage Systems Don't Help Us Save Stuff Forever</i> Mary Baker, Kim Keeton (HP Labs), Sean Martin (British Library, UK) <i>What Dependability for Networks of Mobile Sensors ?</i> Carole Delporte-Gallet, Hugues Fauconnier (University of Paris, France), Rachid Guerraoui (EPFL, Switzerland) <i>Application Communities: Using Monoculture for Dependability</i> Michael E. Locasto, Stelios Sidiroglou, Angelos D. Keromytis (Columbia University, USA) <i>TACID Transactions</i> Marco Vieira (University of Coimbra, Portugal), António C. Costa (University of Lisbon, Portugal), Henrique Madeira (University of Coimbra, Portugal) <i>Q&A Mini-panel</i> Mary Baker, António Costa, Rachid Guerraoui, Angelos Keromytis	Fast Abstracts Poster Session
15:00	Excursion, cultural event and dinner cruise (Buses will leave PACIFICO YOKOHAMA at 15:10) Sankeien Garden Noh Play Royal Wing Dinner Cruise				
15:10					
22:00					

July 1 (Fri) – Morning

08:00 08:30	DSN Registration				
08:30	< Room : 301+302 >			< Room : 311+312 >	
	Panel : Dependability Benchmarking of Computing Systems Panelists: Cristian Constantinescu (Intel Corp.) Henrique Madeira (University of Coimbra) Brendan Murphy (Microsoft Research) Karama Kanoun (LAAS-CNRS) Ira Pramanick (Sun Microsystems) Lisa Spainhower (IBM Corporation)			Workshop 3: Assurance of Networking Systems Dependability Service Level Agreements Chair : Saida Benlarbi (Alcatel, Canada) <i>Overview of the areas and topics of the workshop</i> S. Benlarbi (Alcatel, Canada) <i>Scalable and Resilient to Denial-of-Service Attacks Overlay Networks</i> D.R. Avresky and Y. Varoglu (Northeastern University, USA)	
10:30					
10:30 11:00	Coffee Break				
11:00	< Room : 303 >	< Room : 301+302 >	< Room : 304 >	< Room : 311+312 >	< Room : 313+314 >
	Session 7A: Consensus Chair : Xavier Défago (Japan Advanced Institute of Science and Technology, Japan) <i>Fast Byzantine Consensus</i> J.-P. Martin and L. Alvisi (UT Austin, USA) <i>A Hybrid and Adaptive Model for Fault-Tolerant Distributed Computing</i> S. Gorender, R. Macêdo (LaSiD/DCC, Federal Univ of Bahia, Brazil), and M. Raynal (IRISA/INRIA, France) <i>Adaptive Indulgent Consensus</i> L. Sampaio and F. Brasileiro (Universidade Federal de Campina Grande, Brazil)	Session 7B: Hardware and Codesign Chair : Antonio Casimiro (University of Lisbon, Portugal) <i>Microarchitecture-Based Introspection: A Technique for Transient-Fault Tolerance in Microprocessors</i> M. K. Qureshi, O. Mutlu, and Y. N. Patt (University of Texas at Austin, USA) <i>Reversible Fault-Tolerant Logic</i> P. O. Boykin (University of Florida, USA) and V. P. Roychowdhury (University of California, USA) <i>Co-design Based Approach to Improve Robustness in Networked Control Systems</i> S. Kowshik, G. Baliga (University of Illinois at Urbana-Champaign, USA), S. Graham (Air Force Institute of Technology, USA),, and L. Sha (University of Illinois at Urbana-Champaign, USA)	Session 7C: Experimental Microprocessor Evaluation Chair : Arun Somani (Iowa State University, USA) <i>Neutron SER Characterization of Microprocessors</i> C. Constantinescu (Intel Corp., USA) <i>Microprocessor Sensitivity to Failures: Control vs Execution and Combinational vs Sequential Logic</i> G. P. Saggese, A. Vetteth, Z. Kalbarczyk, and R. K. Iyer (University of Illinois at Urbana-Champaign, USA) <i>Engineering Over-Clocking: Reliability-Performance Trade-Offs for High-Performance Register Files</i> G. Memik (Northwestern University, USA), M. H. Chowdhury (University of Illinois at Chicago, USA), A. Mallik, and Y. I. Ismail (Northwestern University, USA)	Workshop 3 (continued) Chair : Saida Benlarbi (Alcatel, Canada) <i>Cluster-Based Load-Balanced Fault-Tolerant Beacon Vector Routing For Wireless Sensor Networks</i> L. Demoracski and D.R. Avresky (Northeastern University, USA) <i>Dependable Communication Using Multiple Network Paths</i> Y. Kodama, T. Kudoh, and S. Sekiguchi (National Institute of Advanced Industrial Science and Technology, Japan)	Industry Session [II] Chair : Tohru Kikuno (Osaka University, Japan) <i>A Dependable and Cost-Effective Vehicle Control Architecture for X-By-Wire Systems Based on Autonomous Decentralized Concept</i> Kentaro Yoshimura, Kohei Sakurai, Yuichiro Morita, Nobuyasu Kanekawa, Kenichi Kurosawa, Yoshiaki Takahashi, Shigetoshi Sameshima, Akitoshi Shimura (Hitachi, Ltd., Japan) <i>R3: A Framework for Benchmarking System Availability</i> Ji Zhu, James Mauro, Ira Pramanick (Sun Microsystems, USA) <i>FT-JMS: An Open-source Fault Tolerant Java Messaging Service (JMS) Platform</i> Liang-Kai Chu (ICE Technology Corporation, Taiwan), Yennun Huang (AT&T Labs, Taiwan), Sy-Yen Kuo (National Taiwan University, Taiwan)
12:30					
12:30	< Room : Pacific > Lunch			< Room : Lounge > Lunch (2006 PC Kickoff Meeting)	
13:30					

July 1 (Fri) – Afternoon

13:30	< Room : 303 > Session 8A: Dependability Modeling and Prediction Chair : Felicita Di Giandomenico (ISTI-CNR, Italy) <i>H-RAFT - Heuristic Reachability Analysis for Fault Tolerance Protocols Modelled in SDL</i> S. Böhm (University of Duisburg-Essen, Germany) <i>Filtering Failure Logs for a BlueGene/L Prototype</i> Y. Liang, Y. Zhang (Rutgers University, USA), A. Sivasubramaniam (PennState, USA), R. Sahoo, J. Moreira, and M. Gupta (IBM Research, USA) <i>Design Time Reliability Analysis of Distributed Fault Tolerance Algorithms</i> E. Latronico and P. Koopman (Carnegie Mellon University, USA) <i>SoftArch: an Architecture Level Tool for Modeling and Analyzing Soft Errors</i> X. Li (IBM, USA), S. Adve (University of Illinois at Urbana-Champaign, USA), P. Bose, and J. Rivers (IBM, USA)	< Room : 301+302 > Session 8B: Intrusion Detection and Tolerance Chair : William H. Sanders (University of Illinois at Urbana-Champaign, USA) <i>ADEPTS: Adaptive Intrusion Response Using Attack Graphs in an E-Commerce Environment</i> B. Foo, Y.-S. Wu, Y.-C. Mao, S. Bagchi, and E. Spafford (Purdue University, USA) <i>Neutralization of Errors and Attacks in Wireless Ad Hoc Networks</i> C. Basile, Z. Kalbarczyk, and R. K. Iyer (University of Illinois at Urbana-Champaign, USA) <i>Modeling and Automated Containment of Worms</i> S. Sellke, N. Shroff, and S. Bagchi (Purdue University, USA) <i>Fatih: Detecting and Isolating Malicious Routers</i> A. T. Mizrak, Y.-C. Cheng, K. Marzullo, and S. Savage (University of California, San Diego, USA)	< Room : 304 > Session 8C: Replication and Checkpointing Protocol Evaluation Chair : Brendan Murphy (Microsoft Research, UK) <i>Analysis of Probabilistic Trapezoid Protocol for Data Replication</i> T. Suzuki, M. Ohara, M. Arai, S. Fukumoto, and K. Iwasaki (Tokyo Metropolitan University, Japan) <i>Testing the Dependability and Performance of Group Communication Based Database Replication Protocols</i> A. Sousa, J. Pereira, L. Soares, A. Correia Jr., L. Rocha, R. Oliveira, and F. Moura (U. Minho, Portugal) <i>Model-Based Failure Analysis of Journaling File Systems</i> V. Prabhakaran, A. C. Arpaci-Dusseau, and R. H. Arpaci-Dusseau (University of Wisconsin, Madison, USA) <i>Modeling Coordinated Checkpointing for Large-Scale Supercomputers</i> L. Wang, K. Pattabiraman, Z. Kalbarczyk, R. K. Iyer (University of Illinois at Urbana-Champaign, USA), L. Votta, C. Vick and A. Wood (Sun Microsystems, USA)	< Room : 311+312 > Workshop 3 (continued) Chair : Saida Benlarbi (Alcatel, Canada) • Round-table discussion about the two main topics: Estimation issues and Modeling issues of Assuring • Converged Networking Systems • Discussions of covered topics in work groups • Gathering to compare feedback and identify common understanding, issues and directions of the topic • Summary and concluding remarks	< Room : 313+314 > Industry Session [III] Chair : Tohru Kikuno (Osaka University, Japan) <i>Dependable Digital Automatic Train Control System</i> Masayuki Matsumoto (East Japan Railway Company, Japan) <i>Standards and Methodologies for Validation of Complex Railway Systems</i> Amendola Arturo, Esposito Rosaria, Impagliazzo Leonardo, Marmo Pietro, Poli Fabio (Ansaldo Segnalamento Ferroviario S.p.A., Italy)
15:30					
15:30	Coffee Break				
16:00					
16:00	< Room : 301+302 > Business Meeting : IEEE Technical Committee on Fault-Tolerant Computing (All participants are invited.)				
17:00					

Fast Abstracts < Room : 313+314 >

[I] : June 29 (Wed) 15:30 – 17:00	[II] : June 30 (Thu) 08:30 – 10:00
Chair : Antonio Casimiro Costa (University of Lisbon, Portugal) 1. <i>A Novel Recurrent Neural Network Controller for Dynamic Buffer Size Tuning to Provide More Reliable End-to-End Client/Server Interaction</i> Wilfred W. K. Lin (The Hong Kong Polytechnic University, China), Tharam S. Dillon (University of Technology, Sydney, Australia), and Allan K. Y. Wong (The Hong Kong Polytechnic University, China) 2. <i>Fault Tolerant SCADA with CORBA on LANE ATM</i> David Selvakumar, Chester Rebeiro (Centre for Development of Advanced Computing, India), R Pitchiah (Department of Information Technology, India) 3. <i>Communication-efficient Implementation of Failure Detector Class <math>\leftrightarrow P</math></i> Mikel Larrea and Alberto Lafuente (The University of the Basque Country, Spain) 4. <i>Replication Buffering Relay Method: Storage-based Replication over Long Distances with No-data-loss for Disaster Recovery</i> Masaki Kan, Jun-ichi Yamato, Yuji Kaneko, and Yoshihide Kikuchi (NEC Corporation, Japan) 5. <i>A Failure Rate Prediction Method for the Probabilistic Safety Assessment</i> Ji-Young Kim (Chungnam National University, Korea), Dong-Young Lee (Korea Atomic Energy Research Institute, Korea), and Joon Lyoo (Chungnam National University, Korea) 6. <i>Run Time Encoding of Function Pointers by Dynamic Linker</i> Gyungho Lee (University of Illinois at Chicago, USA), Changwoo Pyo (Hongik University, Korea), and Tae-Jin Kim (University of Illinois at Chicago, USA) 7. <i>An Evaluation of Mean Delay and Jitter for 802.11e WLAN</i> Norifumi Ikeda, Hiroei Imai (Niigata University, Japan), Masahiro Tsunoyama (Niigata Institute of Technology, Japan), and Ikuro Ishii (Niigata University, Japan) 8. <i>A Method of Software Reliability Assessment for Xfce Open Source Project</i> Yoshinobu Tamura (Tottori University of Environmental Studies, Japan), Shigeru Yamada (Tottori University, Japan), and Mitsuhiro Kimura (Hosei University, Japan) 9. <i>Automated Discovery of Buffer Overflow Vulnerabilities in Executable Software without Source-Code</i> João Durães (DEIS-ISEC/CISUC, Portugal) and Henrique Madeira (DEI/CISUC University of Coimbra, Portugal) 10. <i>Describing Inner Data Structure Management in Mobile IPv6 Using Multi-Nodes Finite State Machine</i> Yujun. Zhang and Zhongcheng Li (Institute of Computing Technology, Chinese Academy of Sciences, China) 11. <i>Extinction Time Distribution of Internet Worms in Stochastic Kill-Signal Model</i> Hiroyuki Okamura, Hisashi Kobayashi, and Tadashi Dohi (Hiroshima University, Japan) 12. <i>Efficient Embedded Firewall for Communication Appliances</i> Sachin Garg and Navjot Singh (Avaya Labs Research, USA) 13. <i>A Reasoning Approach to Security Analysis</i> Junichi Miura and Jingde Cheng (Saitama University, Japan) 14. <i>Cost Efficient and Dependable: CAN in Advanced Applications</i> Carl Bergenhem (National Testing and Research Institute, Sweden) and Håkan Sivencrona (Mecel, Delphi Corporation, Sweden)	Chair : François Taïani (Lancaster University, UK) 1. <i>Performance and Performability Evaluations for Networked Humanoid Robot System</i> Takashi Okuda, Yoshimi Sago, Ikuo Maeda, Tetsuo Ideguchi, and Xuejun Tian (Aichi Prefectural University, Japan) 2. <i>Code Comprehension and MBTI Type</i> David Greathead (University of Newcastle upon Tyne, UK) 3. <i>Impact of Comprehensive Security Services on Grid Computing Performance</i> Syed Naqvi and Michel Riguidel (Graduate School of Telecommunications, France) 4. <i>Accessible Formal Verification for Safety-Critical Hardware Design</i> John Lach, Scott Bingham, Carl Elks, Travis Lenhart (University of Virginia, USA), Thuy Nguyen, and Patrick Salaun (Electricité de France, France) 5. <i>Security Issues in Persistently Reactive Systems</i> Takumi Endo, Junichi Miura, Koichi Nanashima, Shouichi Morimoto, Yuichi Goto, and Jingde Cheng (Saitama University, Japan) 6. <i>Detecting IIS Attacks based on Neural-Network</i> Shi-Jinn Horng (Southwest Jiaotong University, National Taiwan University of Science and Technology, Taiwan), Pingzhi Fan, Yao-Ping Chou, and Yen-Cheng. Chang (National Taiwan University of Science and Technology, Taiwan) 7. <i>A Multi-Faced Approach towards Spam-Resistible Mail</i> Ming-Wei Wu (National Taiwan University, Taiwan), Yennun Huang (AT&T Labs, USA), and Sy-Yen Kuo (National Taiwan University, Taiwan) 8. <i>A Formal Method for Verifying Security Specifications Based on International Standard ISO/IEC 15408</i> Shoichi Morimoto, Shinjiro Shigematsu, and Jingde Cheng (Saitama University, Japan) 9. <i>How to Build a Dam: Fighting Application-Level DoS Attacks</i> Gal Badishi (Technion, Israel), Amir Herzberg (Bar-Ilan University, Israel), and Idit Keidar (Technion, Israel) 10. <i>A Robust Service Discovery Approach for Hybrid Ad Hoc Networks</i> Sung-Hee Lee and Young-Bae Ko (Ajou University, Korea) 11. <i>A Study on Open Network Communication by Using Data Diversity</i> Keiji Miura and Masayoshi Sakai (The Nippon Signal Co.,Ltd., Japan) 12. <i>Surviving Survivability Specifications</i> Lucia Cloth and Boudewijn R. Haverkort (University of Twente, the Netherlands) 13. <i>A Routing Protocol with Directional Antennas for Improving Robustness in Mobile Ad Hoc Networks</i> Sung-Ho Kim and Young-Bae Ko (Ajou University, Korea)

Fast Abstracts < Room : 313+314 >

【III】: June 30 (Thu) 10:30 – 12:00	
Chair : Matti Hiltunen (AT&T Labs - Research, USA)	
1. <i>Comparing Fault Recovery Mechanisms for Superscalar Processors</i> Toshinori Sato (PRESTO, Japan Science and Technology Agency, Japan)	
2. <i>Fault-Tolerant CMP Design Using a Write Cache Checker</i> Elias Mizan (The University of Texas at Austin, USA) and Marcos De Alba (Tecnologico de Monterrey, Mexico)	
3. <i>Dependability Evaluation of Mobile Devices' System Software</i> Mário Zenha-Rela, Marco Vieira (University of Coimbra, Portugal), João Carlos Cunha, and João Durães (Polytechnic Institute of Coimbra, Portugal)	
4. <i>Performability Modeling for a Calendar Server</i> Sarma Vempati, Dong Tang, Pedro Vazquez, and Swami Nathan (Sun Microsystems, Inc., USA)	
5. <i>Proactive Fault Manager for High Performance Computing</i> Yawei Li and Zhiling Lan (Illinois Institute of Technology, USA)	
6. <i>Towards Replication of Web Services in WANs</i> Jorge Salas, Francisco Perez-Sorrosal, Marta Patiño-Martínez and Ricardo Jiménez-Peris (Universidad Politécnica de Madrid, Spain)	
7. <i>Continuous Access to Remote Devices in the Presence of Device Migration</i> Ryota Ozaki (The Graduate University for Advanced Studies, Japan), Soichiro Hidaka, Kazuya Kodama, and Katsumi Maruyama (National Institute of Informatics, Research Organization of Information and Systems, The Graduate University for Advanced Studies, Japan)	
8. <i>Address Space Obfuscation to Tolerate Windows Code Injection Attacks</i> Tufan Demir, Karl Levitt, Lynn Nguyen, and Jeff Rowe (UC Davis, USA)	
9. <i>Proactive Problem Determination in Transaction-Oriented Applications</i> Soila Pertet, Priya Narasimhan (Carnegie Mellon University, USA), Anca Sailer, and Gautam Kar (IBM T.J. Watson Research Center, USA)	
10. <i>Epidemic Multicast with Optimal Node Selection in Ad-Hoc Networks</i> Ruben Torres, Vinita Apte, and Saurabh Bagchi (Purdue University, USA)	
11. <i>Reliable Data Dissemination using Trust in Multi-hop Sensor Networks</i> Ravish Khosla, Yu Cheng, and Saurabh Bagchi (Purdue University, USA)	
12. <i>Resource Fault Prediction for Fine-Grained Cycle Sharing</i> Xiaojuan Ren, Seyong Lee, Saurabh Bagchi, and Rudolf Eigenmann (Purdue University, USA)	
13. <i>Overview of a New CNES Architectural EDAC Concept for High Speed Memory Protection</i> Michel Pignol (CNES, France)	
14. <i>Challenges in Realizing Distributed Adaptive Dependability</i> Jean-Charles Fabre (LAAS-CNRS, France), Priya Narasimhan (Carnegie Mellon University, USA)	

Fast Abstracts – Poster Session – < Room : 302 >

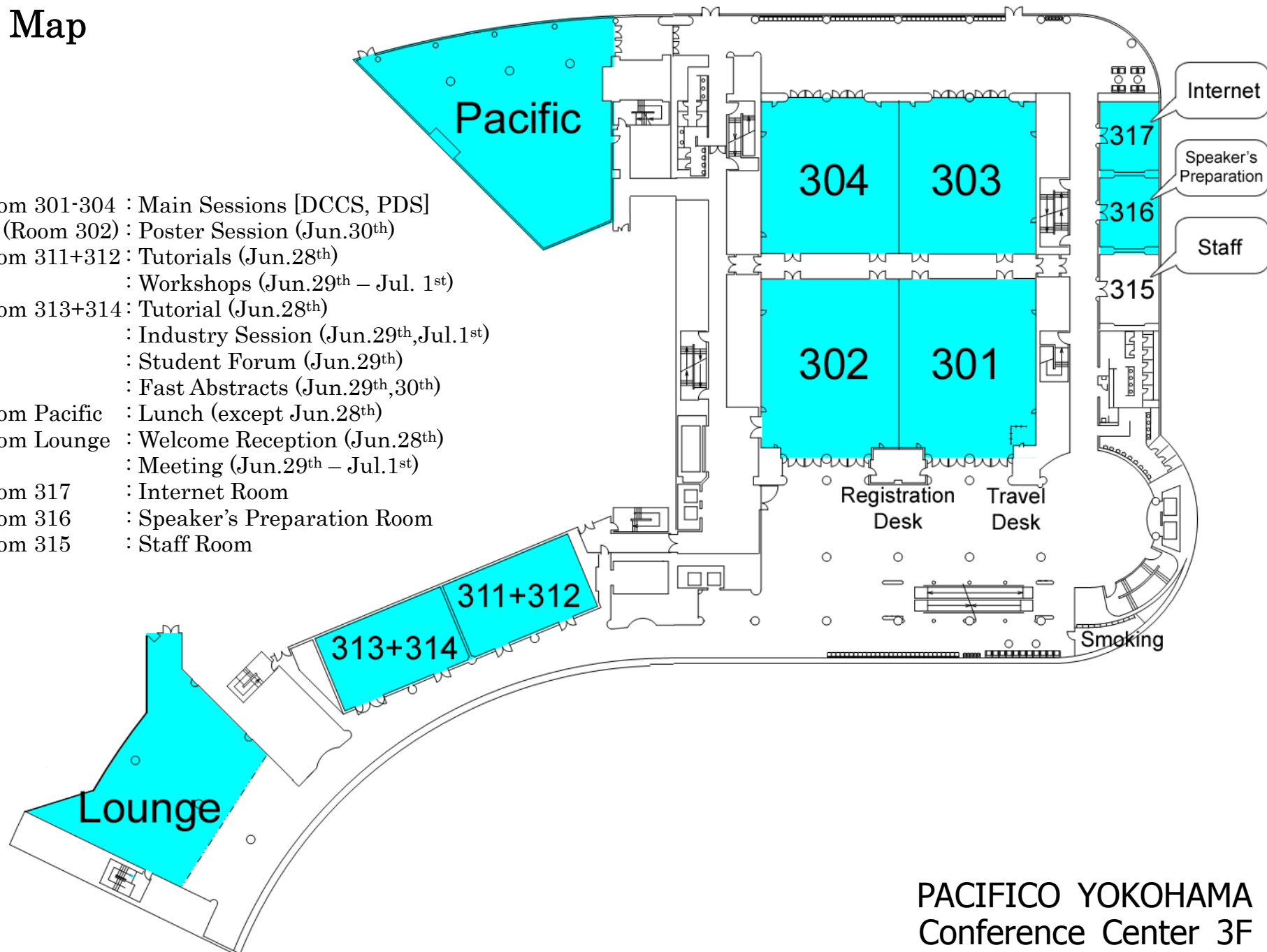
June 30 (Thu) 13:00 – 14:45	

Workshop 2 [Papers with Posters] < Room : 311+312 >

June 30 (Thu)	
• <i>Handling Cascading Failures: The Case for Topology-Aware Fault Tolerance</i> Soila Pertet, Priya Narasimhan (Carnegie Mellon University, USA)	
• <i>The Case for an Internet Health Monitoring System</i> Matthew Caesar, Lakshminarayanan Subramanian, Randy H. Katz (University of California, Berkeley, USA)	
• <i>Rewriting "The Turtle and the Hare": Sleeping to Get There Faster</i> José Pereira, Rui Oliveira (University of Minho, Portugal)	
• <i>Time-varying Management of Data Storage</i> Ranjita Bhagwan, Fred Douglass, Kirsten Hildrum, Jeffrey O. Kephart, William E. Walsh (IBM T.J. Watson Research Center, USA)	
• <i>Managing Self-Inflicted Nondeterminism</i> Dmitrii Zagorodnov (University of Tromsø), Keith Marzullo (University of California, San Diego, USA)	

Floor Map

- Room 301-304 : Main Sessions [DCCS, PDS]
- (Room 302) : Poster Session (Jun.30th)
- Room 311+312 : Tutorials (Jun.28th)
- : Workshops (Jun.29th – Jul. 1st)
- Room 313+314 : Tutorial (Jun.28th)
- : Industry Session (Jun.29th, Jul.1st)
- : Student Forum (Jun.29th)
- : Fast Abstracts (Jun.29th, 30th)
- Room Pacific : Lunch (except Jun.28th)
- Room Lounge : Welcome Reception (Jun.28th)
- : Meeting (Jun.29th – Jul.1st)
- Room 317 : Internet Room
- Room 316 : Speaker's Preparation Room
- Room 315 : Staff Room



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The International Conference on Dependable Systems and Networks 2005

June 28 – July 1, 2005
PACIFICO YOKOHAMA Conference Center
Yokohama, Japan

DSN-2005 Session Organization

June 28 (Tue)

Tutorial A	
Tutorial C	Tutorial E

June 29 (Wed)

Keynote				
1A (DCCS)	1B (DCCS)	1C (PDS)	Workshop 1	Industry [I]
2A (DCCS)	2B (DCCS)	2C (PDS)	Workshop 1	Student Forum
3A (DCCS)	3B (DCCS)	3C (PDS)	Workshop 1	Fast Abstracts [I]

June 30 (Thu)

4A (DCCS)	4B (DCCS)	4C (PDS)	Workshop 2	Fast Abstracts [II]
5A (DCCS)	5B (DCCS)	5C (PDS)	Workshop 2	Fast Abstracts [III]
6A (DCCS)	6B (DCCS)	6C (PDS)	Workshop 2	Fast Abstracts (Poster)

July 1 (Fri)

Panel			Workshop 3	
7A (DCCS)	7B (DCCS)	7C (PDS)	Workshop 3	Industry [II]
8A (DCCS)	8B (DCCS)	8C (PDS)	Workshop 3	Industry [III]

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